

Abstracts

2.7 volt performance of high dynamic range receiver ICs for dual-band CDMA handsets

K. Rovos, F. Ali, C. Wakeham, B. Khabbaz, M. Murphy and K. Harrington. "2.7 volt performance of high dynamic range receiver ICs for dual-band CDMA handsets." 1999 Radio Frequency Integrated Circuits (RFIC) Symposium 99. (1999 [RFIC]): 73-76.

This paper presents the design and performance of two highly integrated receiver ICs developed for the CDMA mobile handset. The receivers operate at 2.7 volt from a single supply and are designed for low noise figure and high IP3 in order to meet the stringent requirements of IS-98 and J-STD-018. The first IC operates in the cellular band (800 MHz) while the other one operates in the PCS band (1900 MHz).

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